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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	Not Yet Assigned 10/763,255		
		Filing Date	January 26, 2004		
		First Named Inventor	Takamitsu Yamada		
		Art Unit	Not Yet Assigned 2138		
		Examiner Name	Not Yet Assigned CALUNG		
Sheet	1	of	1	Attorney Docket Number	R2180.0188/P188

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ²	Number-Kind Code ³ (If known)				
gmc	BA	2002-56044	JPX	02/20/2002	Mitsubishi Electric Corp.		
gmc	BB	10-242283	JPX	09/11/1998	Hitachi Ltd.		
gmc	BC	2002-203001	JPX	07/19/2002	Toshiba Corp.		
gmc	BD	2000-115147	JPX	04/21/2000	Seiko Epson Corp.		
gmc	BE	2000-261310	JPX	09/22/2000	Hitachi Ltd. et al.		
gmc	BF	2000-332733	JPX	11/30/2000	Nec Corp.		

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ³
gmc	CA	"A Token Scan Architecture for Low Power Testing", p. 660, Proc. International Test Conference 2001, Oct. 30, 2001, Baltimore, MD USA.	

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Examiner Phung My Chung	Date 2/22/06
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RAP
4/13/08